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## AT09363: PTC Robustness Design Guide

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### APPLICATION NOTE

## Introduction

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The capacitive touch interfaces are increasingly becoming popular in all electronics goods. To seamlessly integrate with wide variety of appliances the capacitive touch interfaces must exhibit robust operation.

The Peripheral Touch Controller (PTC) is a hardware module providing high touch performance while achieving best-in class noise immunity, moisture tolerance, and faster response time. This document describes the guidelines, tips, and tricks to improve the robustness of PTC based touch designs.

## Features

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- Factors affecting noise performance, moisture tolerance, and response time
- Hardware design considerations
- Tuning guidelines



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## 1. Overview

The capacitive touch interface needs to perform under challenging conditions like noisy environment, exposure to moisture, and varying temperatures. This requires the capacitive touch interface to comply with stringent EMC standards.

This document describes methods to achieve:

- High Noise Immunity
- Moisture Tolerance
- Fast Response Time

### 1.1. Practical Challenges

Noise immunity, moisture tolerance, and response time performances are inter-dependent on each other. Increasing the noise immunity has an adverse effect on the response time. Designing for higher moisture tolerance can reduce noise performance. To achieve faster response time, requires minimum time for touch acquisition and signal post processing that affects the moisture tolerance and noise immunity of the system.



Thus, an efficient touch design must follow the guidelines to achieve the best combination of the robustness features.

## 2. Noise Immunity

Noise immunity simply refers to a product's immunity to unwanted 'noisy' voltages and currents. The source of this unwanted noise can include RF transmitters, switched-mode power supplies, other interconnected devices that have electronic activity in RF range, electrostatic discharge (ESD), lightning, supply voltage fluctuations, load switching, etc. The electronic systems are expected to work reliably under these noisy conditions.

There are test standards that duplicate the behavior of the above said noises and check whether the system operation is predictable. These standards are generally called as "Electromagnetic Compatibility (EMC) Standards". The EMC standards comprises of numerous standards for different types of noise sources.

### 2.1. EMC Standards

This section provides an overview of few typical EMC test standards to which capacitive touch interfaces are subjected to.

#### 2.1.1. IEC 61000-4-2 – Immunity to Electrostatic Discharge (ESD)

ESD refers to a sudden flow of current between two charged object due to static electricity. In general this current is very high and can cause permanent damage to the microcontroller. The main sources of ESD are plug-and-play electronic devices, human contact, etc.

In general, touch applications are touched through a touch panel which is made of dielectric material. So, the touch applications are inherently protected from ESD. Some special attention should be given if the expected ESD stress level is more than the breakdown voltage of the touch panel or if the circuitry is not completely covered by the enclosure.

#### 2.1.2. IEC 61000-4-4 – Immunity to Electrical Fast Transients (EFT)

EFT occurs when a gaseous discharge occurs while opening a switch through which current was flowing. The effect of the transient discharge is huge if the conductor was carrying huge amount of current. The EFT couples with the cables which are closer in proximity and travels on the line until it finds a discharge path.

This charge lasts only for a short duration (in the order of few nanoseconds (ns)). Touch applications are expected to operate normally without any false detect during the test.

#### 2.1.3. IEC 61000-4-5 – Immunity to Surge Voltage

Enormous amount of charge gets formed on the transmission lines when lightning hits transmission lines. This charge gets distributed throughout the line and redirected to earth at various stages. There are cases where this charge is observed even on the main supply of houses. The amount of charge is huge and it is equivalent to a few Kilo-Voltage (KV). Similar kind of charge (with lesser amplitude) gets formed on the lines when huge load is disconnected from the supply in industries or in power-grids.

The charge takes time, in the order of few micro-seconds ( $\mu$ s) to get discharged. Touch applications are expected to operate normally without any false detect during the test.

#### 2.1.4. IEC 61000-4-6 – Immunity to Conducted Disturbances

Conducted noise will generally be in common-mode (CM) and appear across all connecting cables to a device.

Capacitive touch applications are generally not affected by CM noise until human interaction takes place. This is because the power supply lines maintain a stable difference between VDD and GND and as no return path is provided to the noise source reference (usually EARTH), the circuit functions normally.

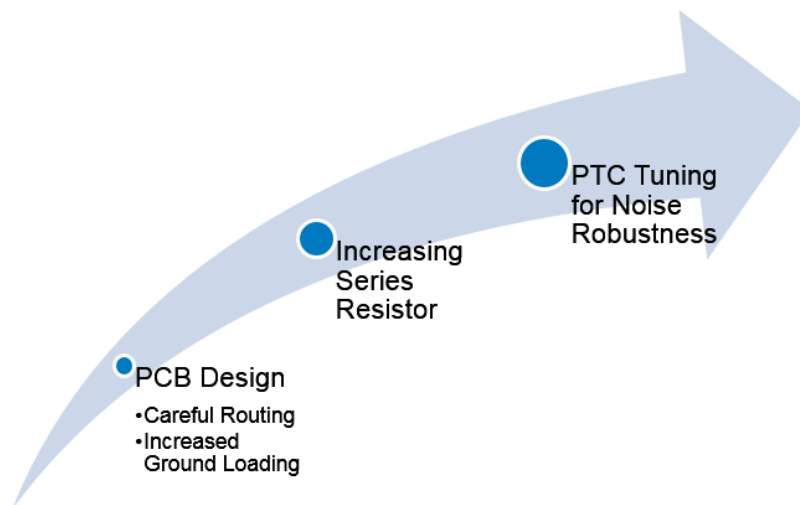
Once human interaction takes place, however, the user's finger now provides a return path and effectively couples noise directly into the capacitive sensor. When this noise reaches levels where normal filtering algorithms become ineffective, errors are introduced into the touch acquisition and the system becomes unreliable. This can manifest itself by way of no touch detect, false detect or in some cases, a complete system lock-up.

## 2.2. Improving Noise Immunity

To utilize suitable techniques and eliminate the effects of unwanted noise disturbances, it is important to understand the environment in which the touch application needs to operate. The noise immunity of a system can be increased significantly by complying with the recommended guidelines throughout the different phases of a design cycle.

Following aspects require careful attention as they can directly affect noise performance:

- Schematic Design
- PCB Design
- Optimizing Series Resistor
- Enclosure
- PTC Configuration

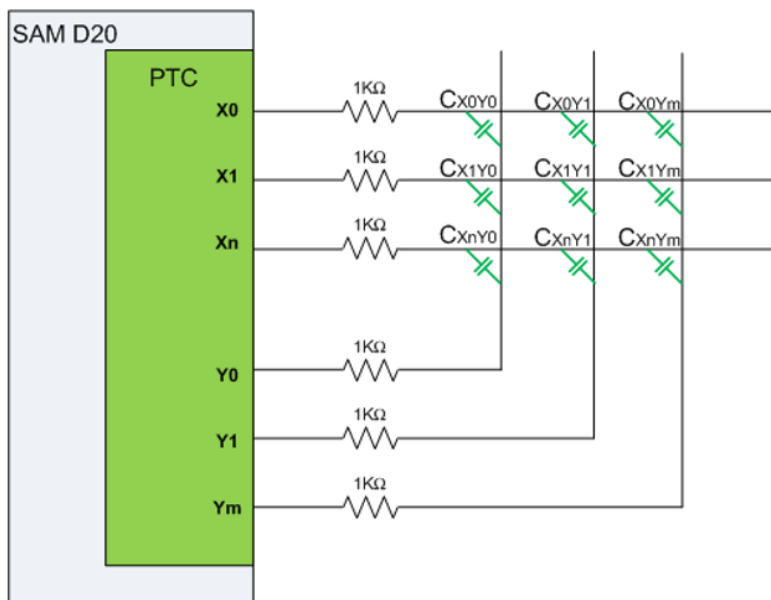


### 2.2.1. Schematic Design

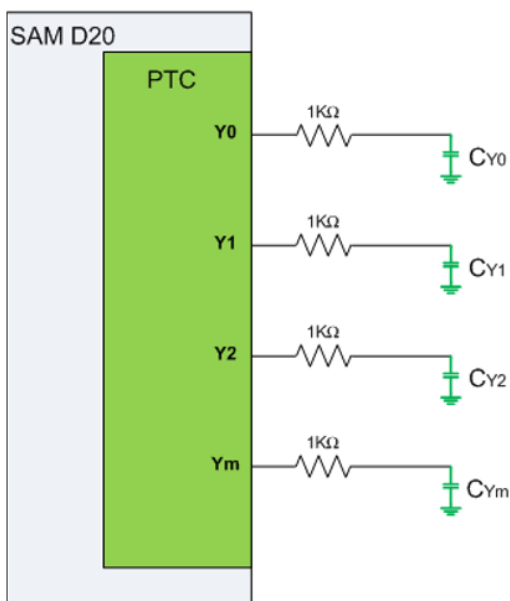
#### External Series Resistor Sense Lines

External Series Resistor Sense Lines Use at least 1k $\Omega$  external series resistor closer to the MCU pin on the sense lines for both self-capacitance and mutual capacitance sensors as shown in following figures. The external series resistor helps to reduce the effect of ESD, if breakdown occurs on the touch panel or if ESD strikes circuit directly.

**Figure 2-1. Mutual Capacitance Series Resistor**



**Figure 2-2. Self-Capacitance Series Resistor**



## General

Some PTC pins have high parasitic capacitance. Using these pins require higher prescaler setting. Higher prescaler setting would affect the response time.

**Note:** PTC pins Y0 and Y2 of SAM D20 and SAM D21 devices have higher parasitic capacitance than the other Y-lines. Using these pins require higher prescaler setting to ensure full charge transfer. With the increased prescaler setting the response time is increased. Atmel recommends using other Y-lines to get best performance operating in extreme noise environments.

## Dedicated Voltage Regulator

It is recommended to use dedicated voltage regulator for higher noise immunity. Use linear voltage regulator preferably as it generates lesser noise compared to switching regulator. Ripple on the regulator

output should be less than  $\pm 5\%$  of supply voltage. If the design intends to use same voltage regulator for all circuit, then initial prototype PCBs must have provision to connect a dedicated voltage regulator to MCU. This provision can be used during various performance testing.

### **Decoupling Capacitor**

Ensure that all the VDD lines of MCU have at least one dedicated decoupling capacitor. Using three capacitors, 1 $\mu$ F, 100nF, and 1nF on each VDD line reduces effect of different frequency band noises. It is recommended to have 100nF decoupling capacitor for RESET line placed closer to MCU. This improves ESD performance.

### **Series Resistor on VDD Lines**

Include a series resistor (10 $\Omega$ ) or ferrite bead, between the regulator and VDD pin(s) of the MCU. This reduces the effect of EFT/Surge on the MCU.

### **Bulk Capacitor**

Use bulk capacitors closer to the voltage regulator. Refer to the voltage regulator datasheet which specifies the typical value and type of the recommended capacitors.

Use 10 $\mu$ F bulk capacitor on VDD line closer to MCU under following conditions.

- If separate plane for VDD is not available.
- If MCU is placed at a distance more than 10cm from voltage regulator.
- If output of the voltage regulator is shared with other circuitry.
- If voltage regulator and MCU are placed on different PCBs.

Ensure that the shunt capacitor's voltage rating is more (in the order of KV) if it is used before EFT suppressor. Otherwise the capacitor can break.

### **Transient Voltage Suppressors (TVS) Diodes**

If the system is expected to operate under high level of ESDs, Transient Voltage Suppressors (TVS) diodes can be used on the sense lines. Ensure that the TVS diodes are placed closer to the area where ESD strike can occur (in most cases it is sensor electrode).

If TVS diodes are used on sense lines ensure that the capacitance of the same less than 1pF.

### **Metal-Oxide Varistors (MOV)**

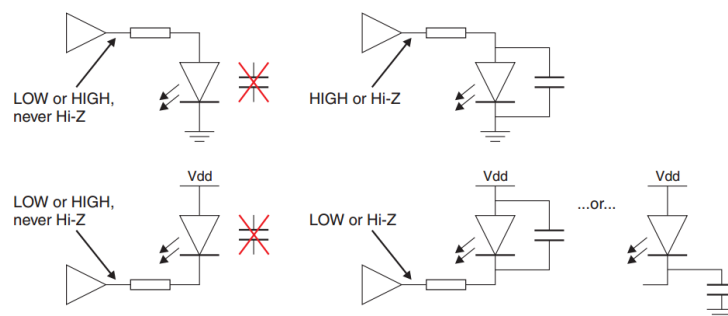
Mains powered applications are likely to suffer due to high voltage surges. MOVs are required to protect the circuit from high voltage surges. Surge voltage varies depending on geographical location. MOVs should be selected based on the geographical location where the application is intended to operate.

### **Bypass Capacitor for LEDs**

The changing capacitance from switching LEDs can cause detection instability and stuck-on state in nearby sensors. This is particularly true if LEDs are pulled down or up to switch on, but are allowed to float when off.

If such LEDs or signal traces for LEDs are less than 4mm away from capacitive sensors, they must be bypassed with a capacitor that has a typical value of 1nF.

**Figure 2-3. Bypass Capacitor for Nearby LEDs**



## 2.2.2. PCB Design

### 2.2.2.1. Sensor Design

In self-capacitance, ensure capacitance of the sensor including the electrode and sensor traces, is not more than 30pF.

In mutual capacitance, ensure capacitance of sensor electrode is not more than 16pF. PTC can compensate for parasitic of X- and Y-traces up to 100pF.

In case of Sliders/Wheels, the capacitance of each channel should not be more than 30pF.

The “Compensation Capacitance (CC)” calibration value can be used to find whether the parasitic capacitance is high. If CC calibration value of a channel is equal to 16383, then the channel is considered to be saturated.

For additional details on how to read the CC calibration value refer to [Atmel® QTouch® Library Peripheral Touch Controller User Guide](#).

For additional details on the sensor design refer to [QTAN0079 Buttons, Sliders, and Wheels Sensor Design Guide](#).

### 2.2.2.2. Layout Considerations

#### Sensor Placement

It is better to group the sensor electrodes to one side of the panel. Placing sensors randomly in the panel will result in unequal parasitic capacitance. To ensure that the sensor electrode which has highest parasitic capacitance is fully charged, the prescaler setting needs to be increased which affects response time.

Grouping the sensors on the panel can help in achieving better moisture tolerance. The moisture tolerance feature “Adjacent Delta Monitoring” operates on a group of sensors which are physically close to each other. Similarly, “Guard Sensor” implementation is easier if the sensors are placed close to each other. For more information on “Adjacent Delta Monitoring” and “Guard Sensor” refer to [Software Filtering](#) and [Guard Sensor](#).

#### Noise Sources

If high noise generating components like, relays, crystal oscillator, inductor, switching regulator, etc., are used ensure that they are properly isolated from the touch circuitry.

#### Stacked PCB Design

In stacked PCB design, ensure that the noise from the other PCB does not affect touch circuitry by properly providing physical gap. In case physical gap is not possible, it is recommended to use a ground plane to isolate the noise from the other PCB.



## Enough Space on Edges

Avoid placing touch sensors close to the edges of the PCB. It is recommended to provide enough clearance between the PCB edge and the sensor electrode. Use of ground traces routed on the PCB edges helps to improve noise immunity.

### Discrete

- Ensure that the series resistors of sense traces are placed close to the MCU pin.
- If series resistors are used for VDD lines, ensure that they are placed closer to the MCU pin.
- Ensure that pull-up resistor and de-coupling capacitor for RESET line are provided and placed closer to the MCU pin.
- Ensure that the de-coupling capacitors are placed closer to the VDD pin.
- Ensure that TVS diodes are placed closer to noise source to ensure that a surge voltage is clamped before the pulse is coupled into adjacent PCB traces.

### 2.2.2.3. Routing

#### Shorter Sensor Traces

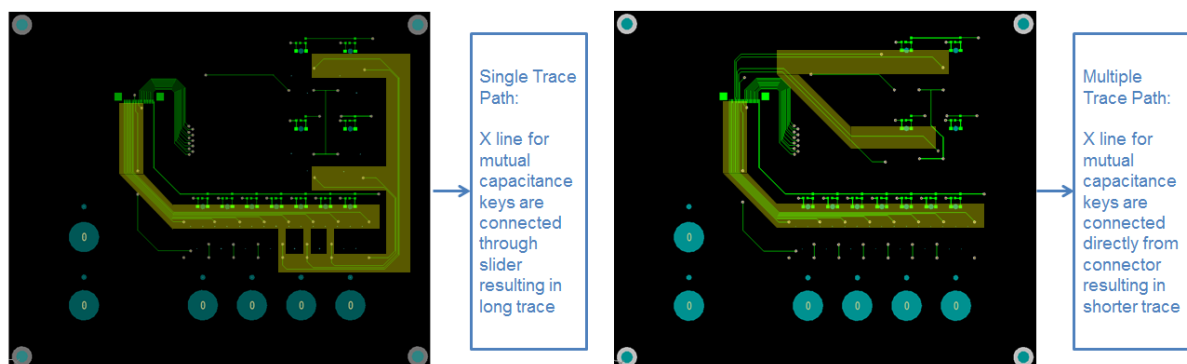
Traces from the microcontroller pins to the sensor electrode should be as short as possible. Making long traces increases loading on the sense line. In practice, using protection mechanism (say ground shielding) is easy to make in short traces comparing to that of longer traces.

The effect of radiated noise is higher on longer traces than compared to shorter traces. To achieve higher noise immunity against radiated noise the trace length should be kept as short as possible (typically <200mm).

In mutual capacitance design, the sensors' traces have higher trace lengths when these traces are routed to multiple sensors due to shared X- and Y-lines. To optimize the trace length, the user can consider using multiple trace paths from connector/MCU pins to each sensor independently instead of a single trace path. This may require more area for routing the traces but will have increased noise immunity to radiated noise.

Following figure shows an example of how using multiple paths reduce the trace length. X-lines are shared between mutual capacitance slider and buttons. For a single trace path, the X-trace length for buttons increase as X-lines are connected from connector to slider and then to buttons. For multiple paths, the X-trace length for buttons decrease as X-lines are connected from connector to buttons directly.

**Figure 2-4. Optimizing Shared Sense Trace Length**



## Enough Space from Edges

Sensor traces should not be routed on the edges of the PCB. A gap of at least 5mm should be provided between sensor traces and PCB edges. The effect of radiated noise is higher on traces close to the

edges compared to those further away from the edges. It is a good practice to have GND trace/plane at the edges as this provides low impedance path for noise.

### Thinner Traces

To prevent false touches over sensor traces keep trace width between 0.1mm and 0.5mm. The sensor traces should be routed on the non-touch side and should be connected to the sensor electrode preferably through a PCB via. This makes the sense traces insensitive to finger touch.

### Nearby Traces

- Sense traces should not be placed near other traces and components, as this may cause loading and interference. Longer sense traces will load the sensor and reduce the sensitivity. Traces with switching signals that are placed close to the sense traces can cause noise in sensors.
- GND traces should not be placed near sense traces. This will load the sensor and reduce the sensitivity. To reduce loading the sense traces and GND traces should cross at 90° on separate layers. If shielding from noise sources is necessary a thin meshed ground may be used behind electrodes (<40% copper). Meshed ground can also be helpful in increasing SNR.

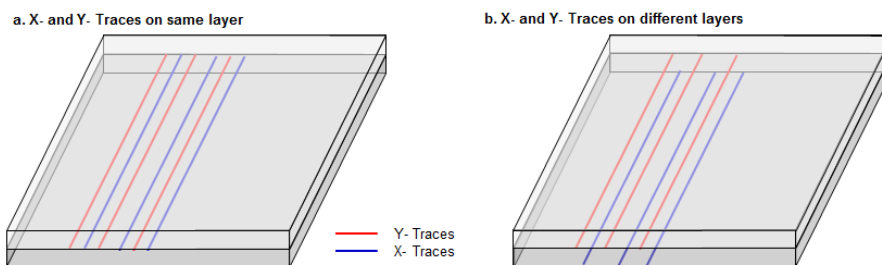
### X- and Y-traces

For mutual capacitance, PTC X- and Y-lines should not be routed for longer lengths. Combination of that X and Y forms a channel and trace area becomes sensitive to touch. It is better to group the X-traces together and Y-traces together and route them physically separated on the PCB.

Following figure shows an example of incorrect X- and Y-traces routing.

- Sufficient gap is not provided between X- and Y-traces.
- X- and Y-traces are not grouped together.

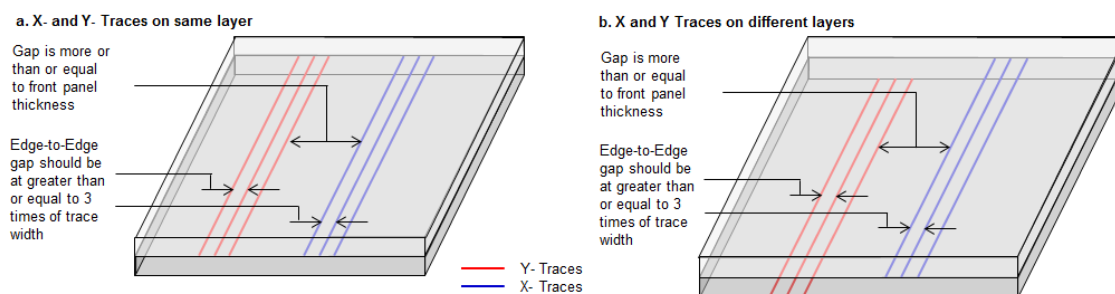
**Figure 2-5. Incorrect X- and Y-traces Routing**



Following figure shows an example of correct X- and Y-traces routing.

- X- and Y-traces are grouped together.
- Sufficient gap is provided between X- and Y-trace groups.

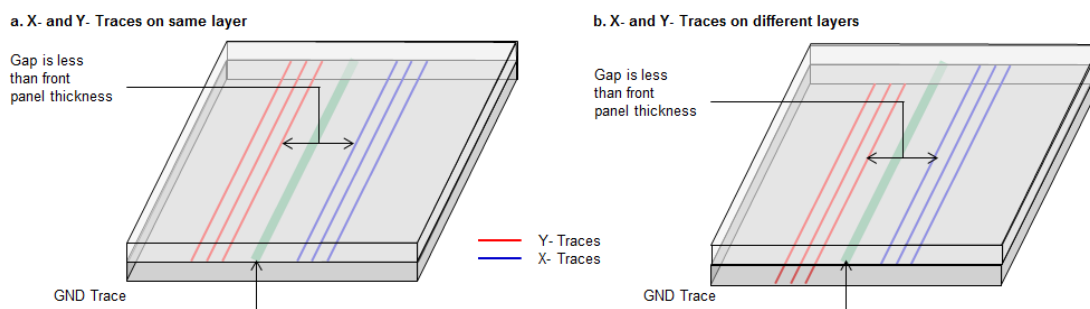
**Figure 2-6. Correct X- and Y-traces Routing – Example 1**



Following figure shows an example of correct X- and Y-traces routing.

- X- and Y-traces are grouped together.
- Not able to provide sufficient gap between X- and Y-trace groups. So, a ground trace is routed between X- and Y-trace groups.

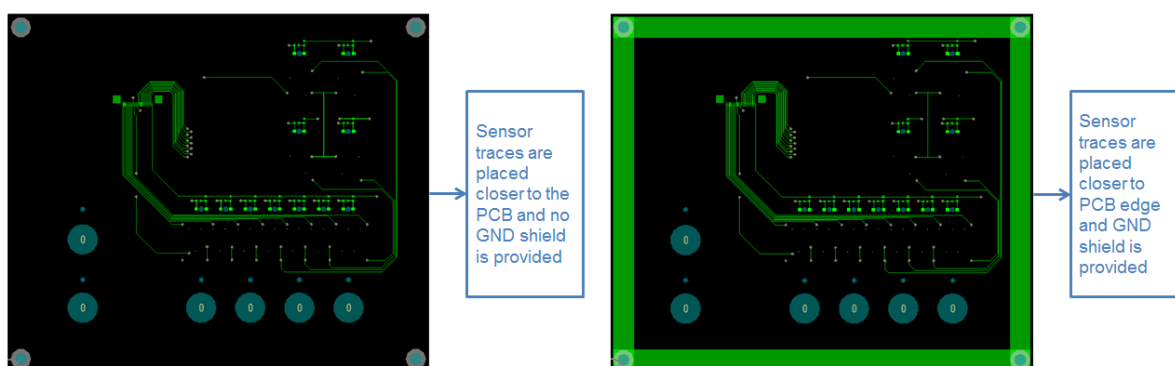
**Figure 2-7. Correct X- and Y-traces Routing – Example 2**



### Ground Shield

In all layers GND trace/plane should surround all other signal traces. This can be achieved by placing a thick ground trace on the edges of the PCB on all layers. Alternatively copper pour can be used in empty space of the PCB and can be connected to GND. The GND trace acts as a barrier between noise and the signal. Using “stitching vias” located closer to each other on the edge of the PCB helps in reducing the effect of noise, even from the sides of the PCB.

**Figure 2-8. GND Shield on PCB Edges**



For higher level of tolerance against radiated noise, hatched GND plane can be placed beneath the sensor electrode. This hatched GND plane should be of 40% or less copper fill.

**Note:** Having ground plane surrounding the sensor electrode reduces moisture tolerance. A compromise between noise reduction and moisture tolerance can be achieved by providing a gap of 3 to 5mm between sensor electrode and ground plane.

#### 2.2.2.4. Improve Return Path (Ground Plane)

Additional improvements in noise immunity can be achieved by providing better return path. This can be in the form of a ground flood on the same plane or as a meshed ground plane behind the sensor. The ground fill provides a low impedance path for the noise, directing it away from the MCUs input pins. Effective return path can be achieved using “stitching vias” to connect ground fill on multiple layers of a PCB.

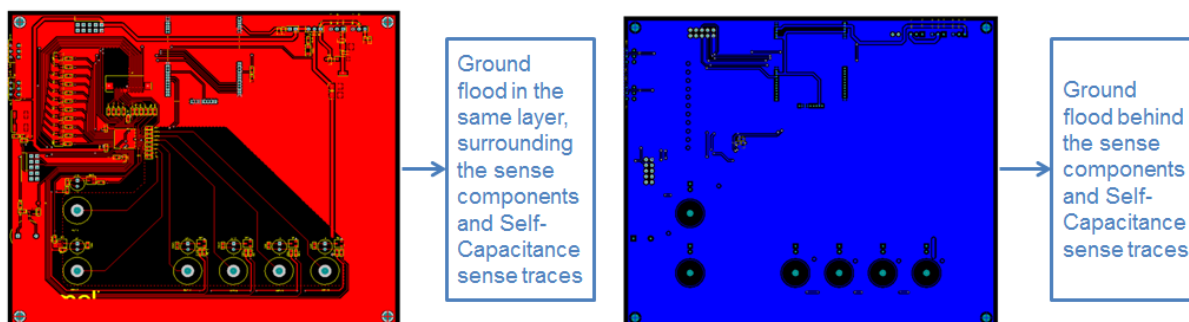
In multi-board systems, the ground connections between boards should be made via multiple pins. The multiple pins should be spread across the board to avoid longer return path. This can be achieved by connecting the mounting holes to ground plane and use conductive screws.

Although adding ground plane reduces sensitivity, the improvement in SNR is much more significant when operating in noisy environments. In most cases, loss of sensitivity due to ground loading can be compensated by adjusting the Gain setting or Detect Threshold.

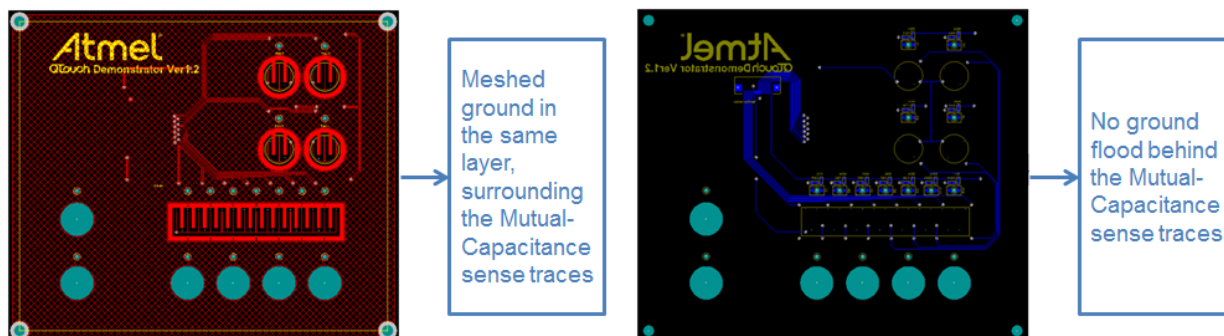
For example, the SAM D20 QTouch Robustness Demo hardware incorporates ground planes in the form of ground floods and meshed ground to improve noise immunity.

Following figures show the ground planes used in SAM D20 QTouch Robustness demo hardware.

**Figure 2-9. Ground Mesh on MCU Board**



**Figure 2-10. Ground Mesh on Sensor Board**



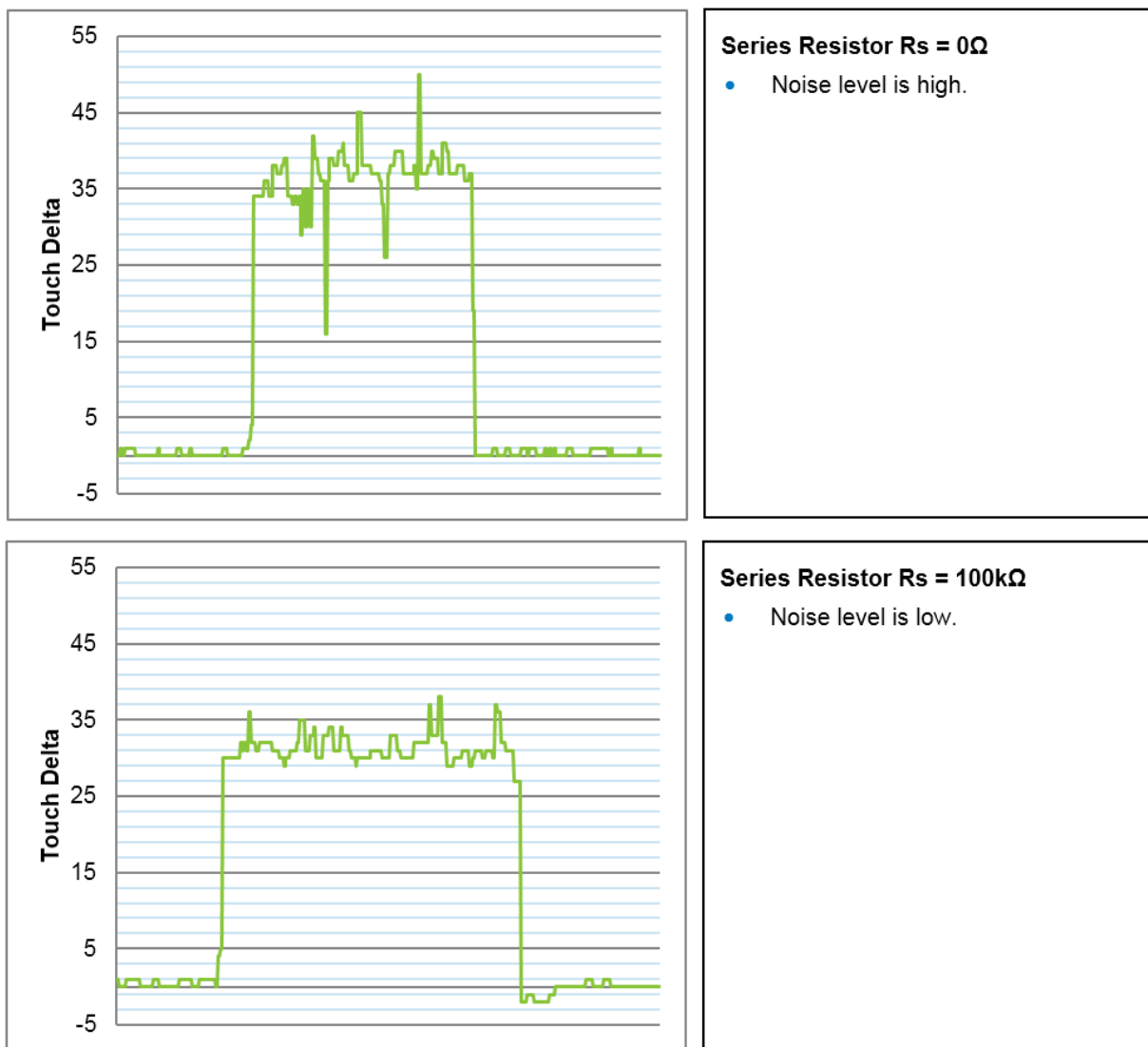
### 2.2.3. Increasing Series Resistor

- Increasing the impedance of the sense traces between the Sensor Electrode and the microcontroller input can significantly improve immunity to conducted noise and ESD. A larger series resistor in the acquisition path provides high impedance to noise while maintaining the overall signal integrity.
- The PTC incorporates an internal series resistor that can be configured to a value ranging from 0Ω to 100kΩ. If ESD occurs, the charge passes through the I/O port before it is attenuated by the internal series resistor. It is better to use an external series resistor, say 1kΩ~100kΩ, for better ESD protection.
- In some extreme environments the internal series resistor of 100kΩ may not be sufficient. In such cases an external series resistor can be used in conjunction with or without the internal series resistor to further increase the impedance of the sense trace. Increasing the external series resistor from a typical value of 1kΩ to 1MΩ, ensuring complete 'charge transfer' can typically improve the noise immunity.
- Depending on the noise disturbance in the given application an optimal series resistor value can be chosen to achieve the required level of noise suppression while meeting other performance criteria such as power consumption and response time.

For example, in SAM D20 QTouch Robustness Demo the self-capacitance sensors use an external series resistor of 220k $\Omega$  in order to pass CI testing at 10V. No internal series resistor was used. In self-capacitance, using external series resistor is more effective compared to internal series resistor.

The mutual capacitance sensors are able to pass CI testing at 10V with an internal series resistor of 100k $\Omega$ . An external series resistor of 1k $\Omega$  is used for ESD protection.

Following figure shows the difference in noise level observed in signal value when the touch system is subjected to conducted noise with varying resistance in the sense path.



Adding a high value Series Resistor will require an increased 'charge transfer' time that increases capacitance measurement time, thereby increasing the response time and power consumption. Refer [Prescaler](#) for the procedure to ensure complete charging of the sensor electrode.

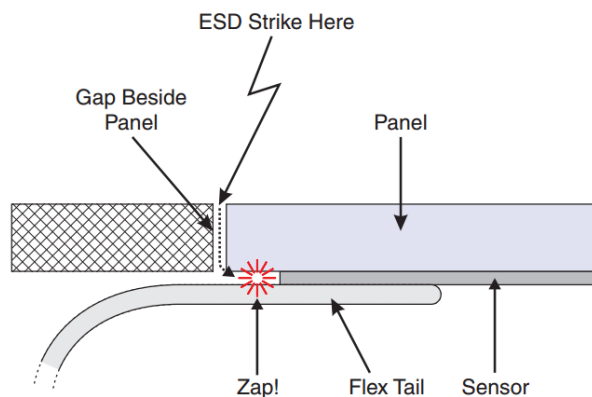
#### 2.2.4. Enclosure

Designing a proper enclosure can help improving the ESD performance. In most applications glass/acrylic is used as touch panel over the touch user interface and plastic as the enclosure for other parts.

In such designs, there is chance to form a small gap between the glass/acrylic and the plastic enclosure. An ESD strike through this gap can cause significant damage.

It would be good to use a thin layer of flexible conducting EMI filter in these gaps. These filters, in general connected to GND, can form a low impedance return path for the external noise and improves the noise immunity.

**Figure 2-11. ESD Penetrates Through Gap in Touch Panel**

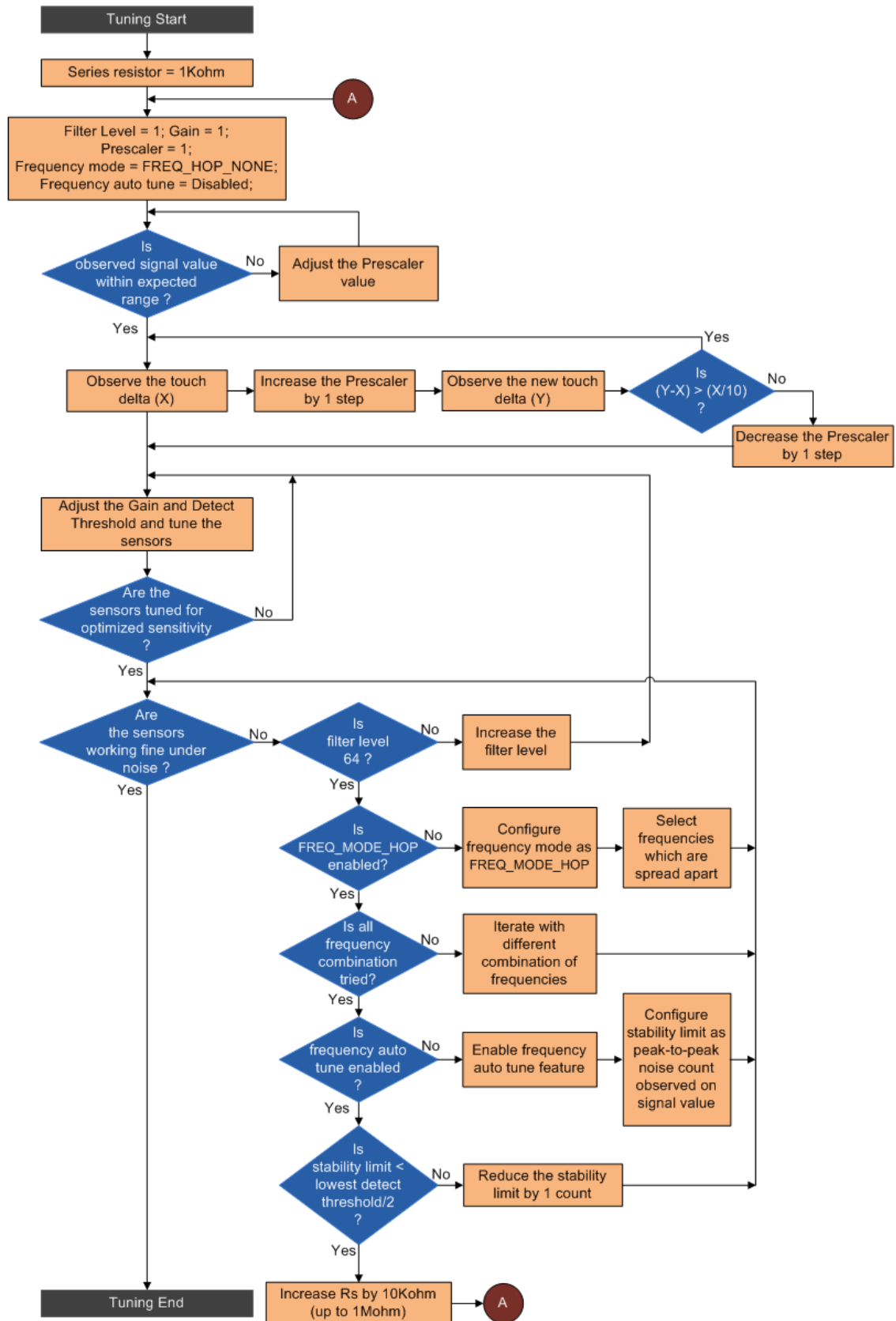


## 2.2.5. PTC Tuning

### 2.2.5.1. PTC Tuning Flow

Following flow chart represents typical flow of tuning PTC parameters. The critical parameter and its effects on touch acquisition are described in subsequent sections.



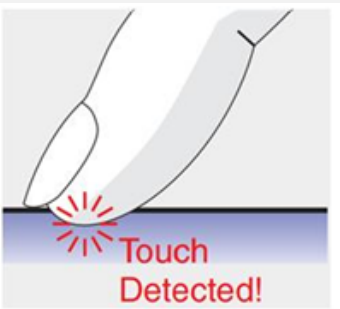
Figure 2-12. PTC Tuning Flow Chart



### 2.2.5.2. Sensitivity Tuning

Before tuning the PTC parameters for better noise performance, the sensors should be tuned for optimized sensitivity. Following table shows differences in sensitivity.

**Table 2-1. Sensitivity Tuning**

Over-Sensitive	Optimally Sensitive	Under-Sensitive
		
If the sensor is reporting detect before contact with the touch surface, then the sensor is oversensitive.	A sensor is at the optimized sensitivity when it only detects the finger as it lightly contacts the touch surface.	If the sensor does not detect the touch or requires a heavy touch to activate, then the sensor is under-sensitive. <b>Note:</b> Capacitive Touch is not sensitive to pressure, but to the expanded area of the flattened finger.
Reduce Gain.	No action required.	Increase Gain.
Increase Detect Threshold.	No action required.	Decrease Detect Threshold.

The optimized sensitivity can be achieved by:

- Ensuring complete charging of the sensor electrode
- Adjusting Gain and Detect Threshold parameters

Section [Prescaler](#) provides details on how to achieve complete charging of the sensor electrode.

Increasing Gain increases sensitivity and vice-versa. The Gain is configured per channel basis. For Slider/Wheel sensors, the Gain needs to be tuned for all the channels. Each step increase in Gain setting increases the touch delta by a factor of 2.

For example, if the touch delta value is 40 with `GAIN_2`, then the same will be ~80 with `GAIN_4`.

Increasing Detect Threshold reduces sensitivity and vice-versa. The Detect Threshold can be configured between 10 and 255.

In the tuning process, the Gain setting should be used to coarse tune the sensitivity and Detect Threshold to fine tune the sensitivity. To achieve optimized sensitivity, both Gain and Detect Threshold should be adjusted.



**Table 2-2. Sensitivity Related Parameters**

Parameter	File
DEF_XXXXCAP_GAIN_PER_NODE	Touch.h
detect_threshold in touch_XXXXcap_sensor_config() API	Touch.h

**2.2.5.3. Prescaler**

In order to obtain stable and repeatable results, it is important to ensure complete charging in each acquisition sample. With the increased series resistor, the rise time of the charge pulse will increase. The duration of the charging pulse needs to be sufficiently long to allow complete charging. This is true even with increased parasitic capacitance.

The prescaler setting determines the clock frequency of the PTC. This in turn affects the acquisition frequency and determines the width of the charging pulse. Complete charging can be ensured by observing the signal and touch delta value.

The expected signal values for a given combination of Filter Level and Gain setting are mentioned in [Atmel QTouch Library Peripheral Touch Controller User Guide](#). If the deviation between the observed signal value and expected signal value is greater than 10 counts, prescaler setting should be increased until the signal value is within the expected range.

**Note:**

The QTouch library provides option to configure different prescaler and series resistor settings during calibration process and periodic touch acquisition. In order to get the expected signal values as mentioned in [Atmel QTouch Library Peripheral Touch Controller User Guide](#), the prescaler and series resistor settings should be the same during calibration and periodic touch acquisition. [Table 2-3 Prescaler Related Parameters](#) lists the macros which configure the prescaler and series resistor during calibration and periodic touch acquisition.

If there is incomplete charging due to higher series resistor or higher parasitic capacitance, the observed touch delta will be less. The prescaler should be adjusted such that sensor electrodes are completely charged. Complete charging can be achieved by selecting the prescaler as follows.

1. Configure the prescaler for which the signal values are within the expected range.
2. Observe the touch delta (X).
3. Increase the prescaler by one step and observe the touch delta (Y).
4. If the difference between Y and X is more than 10% of X, then and go to step B.
5. If the difference in delta value is not more than 10% then reduce the prescaler setting by one step and use it for touch acquisition.

For example, assume the following:

- Signal value is within the expected range with prescaler setting PRSC\_DIV\_SEL\_2
- With PRSC\_DIV\_SEL\_2, the touch delta is 20 counts
- With PRSC\_DIV\_SEL\_4, the touch delta increased to 35 counts
- With PRSC\_DIV\_SEL\_8, the touch delta increased to 38 counts

In this example, increasing the prescaler to PRSC\_DIV\_SEL\_8 is giving only marginal improvement in touch delta comparing to PRSC\_DIV\_SEL\_4. So, it is better to use PRSC\_DIV\_SEL\_4.

**Note:** The mutual capacitance can work with higher PTC clock frequencies comparing to self-capacitance due to low parasitic capacitance.

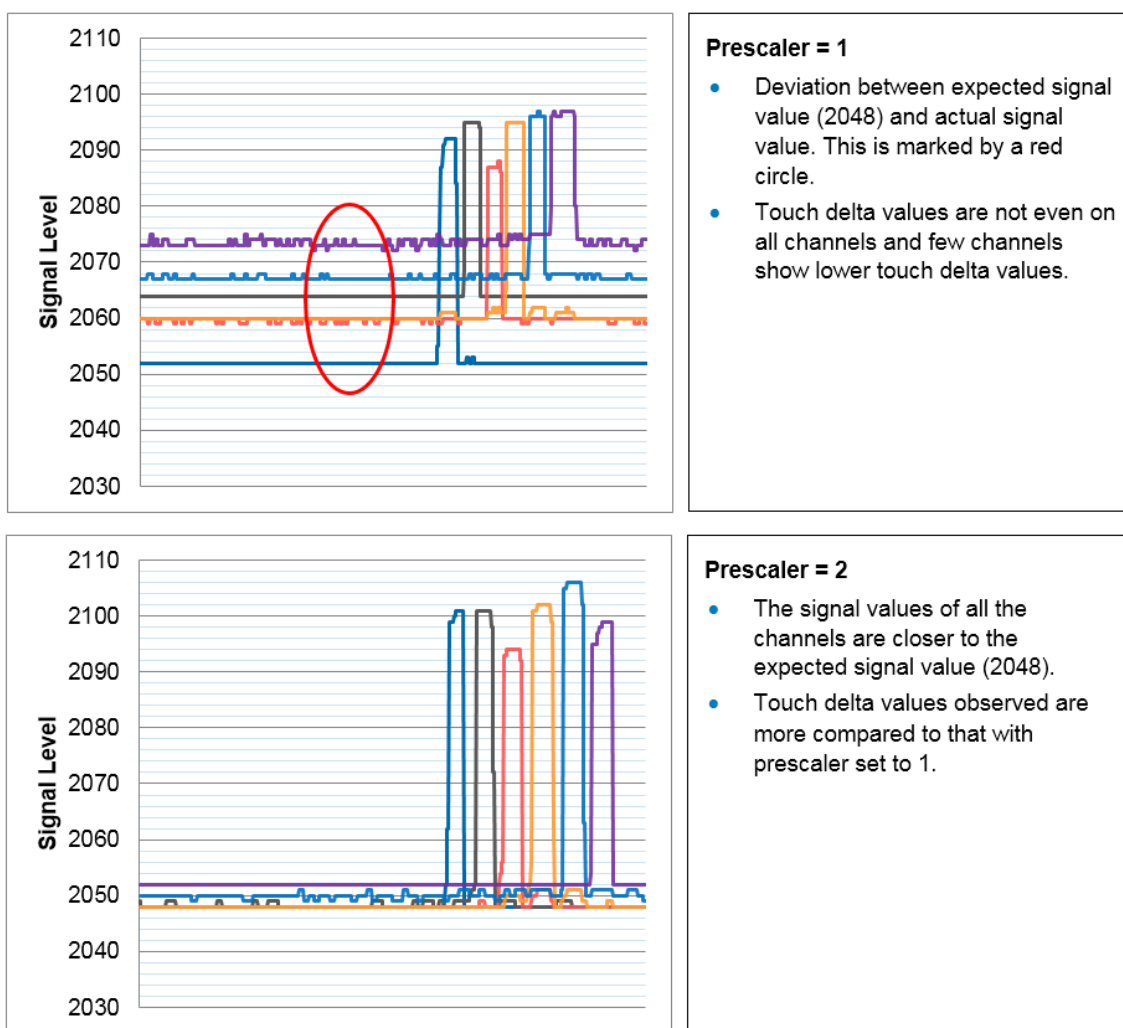
In SAM D20 QTouch Robustness demo:

- Mutual capacitance sensors required a prescaler setting of one (ADC clock = 1MHz) for complete charging with 100k $\Omega$  internal series resistor used. With prescaler = 1, Filter Level = 64, Gain = 4, the signals observed on the Channels were closer to 2048. All signal values observed are within the expected range of 2048  $\pm$ 10 counts.

The self-capacitance sensors required a prescaler setting of two (ADC clock = 512kHz) for complete charging with an external series resistor of 200k $\Omega$  used. With prescaler = 2, Filter Level = 64, Gain = 4, the signals observed on the channels were closer to 2048. All signal values observed are within the expected range of 2048  $\pm$ 10 counts.

Following figure shows the effect of signal value with increasing prescaler setting.

**Figure 2-13. Signal Value Noise Vs Prescaler**



**Table 2-3. Prescaler Related Parameters**

Parameter	File
DEF_XXXXCAP_CC_CAL_CLK_PRESCALE	Touch.c
DEF_XXXXCAP_CC_CAL_SENSE_RESISTOR	Touch.c

Parameter	File
DEF_XXXXCAP_CLK_PRESCALE	Touch.h
DEF_XXXXCAP_SENSE_RESISTOR	Touch.h

#### 2.2.5.4. Charge Share Delay (CSD)

The PTC in SAM C2x, SAM L22 and ATmega328PB devices support configurable Charge Share Delay (CSD). This parameter controls the sensor charge time. Increasing CSD increases sensor charge time and vice-versa.

Both prescaler and CSD parameters control sensor charge time.

The advantages of using CSD parameter are as follows:

- Increasing the prescaler reduces ADC conversion rate but increases charge time. Whereas, increasing CSD parameter increases the charge time and does not affect ADC conversion rate. So, instead of using prescaler, using CSD helps in reducing the overall touch acquisition time for a given charge time.
- The prescaler provides only four selectable clock options. The CSD offers 250 different delay settings. This helps to increase the granularity of the touch acquisition frequency.

The procedure for tuning CSD parameter is same as that of tuning prescaler as mentioned in section [Prescaler](#).

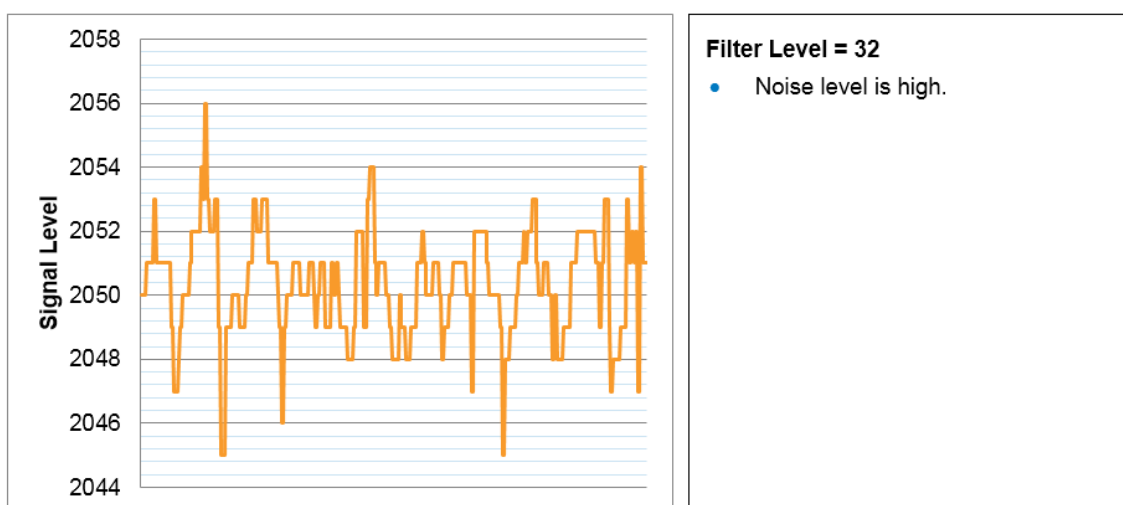
More information about CSD parameter is available in [Migrating QTouch Designs from SAM D MCUs to SAM C MCUs](#).

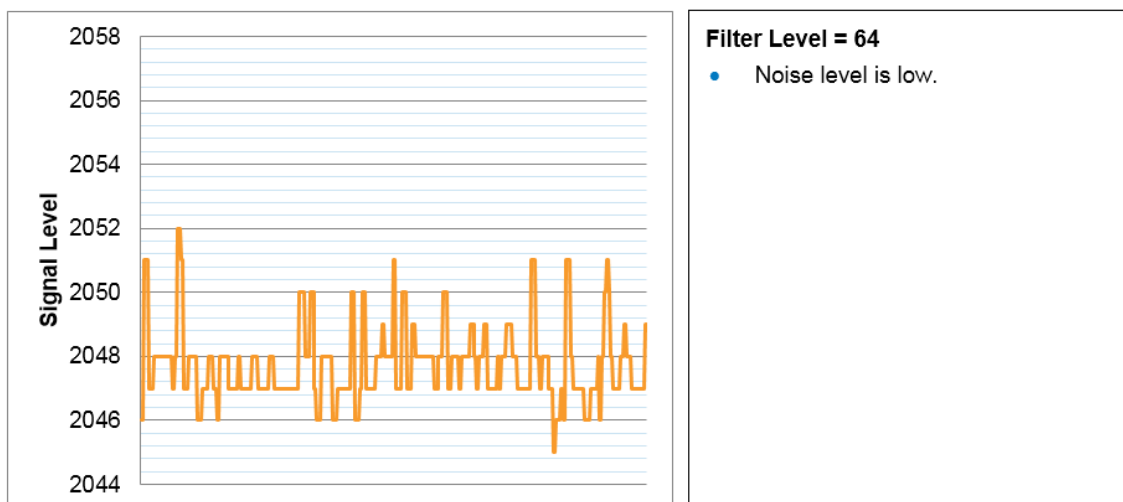
#### 2.2.5.5. Filter Level

The QTouch library provides the option to do over-sampling and averaging. The averaging acts as low-pass filter and reduce the noise level significantly. The DEF\_XXXXCAP\_FILTER\_LEVEL parameter defined in touch.h file determines the over-sample counts. More information on this parameter can be found in [Atmel QTouch Library Peripheral Touch Controller User Guide](#).

Following figure shows the difference in signal variation of a self-capacitance sensor with respect to change in filter level.

**Figure 2-14. Signal Value Noise vs Filter Level**





**Table 2-4. Filter Level Related Parameter**

Parameter	File
DEF_XXXXCAP_FILTER_LEVEL	Touch.h

#### 2.2.5.6. Frequency Mode

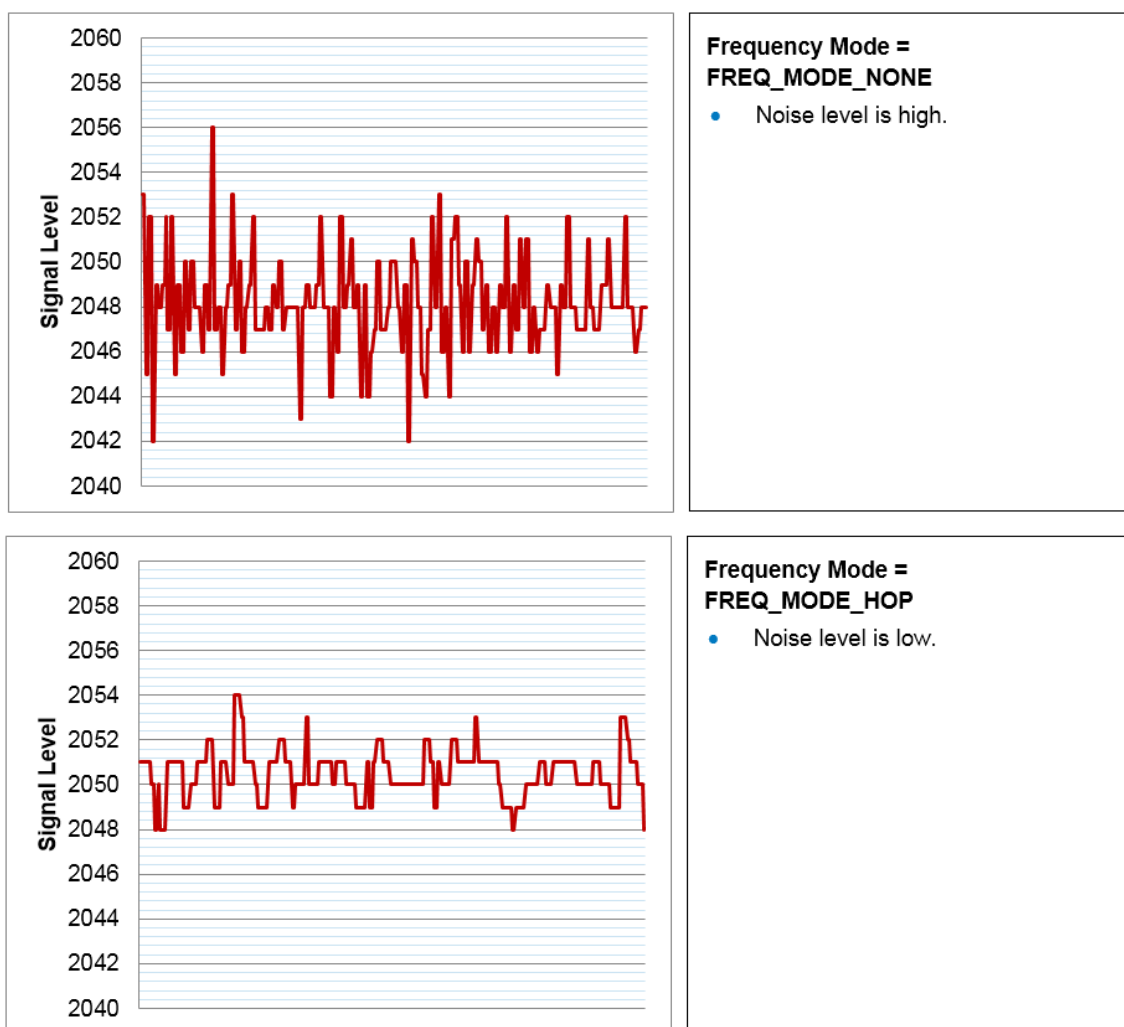
The noise level on the signal value increases if the frequency of the noise is closer to the touch acquisition frequency. If increasing the filter level is not helping in reducing the noise level, then it is better to change the acquisition frequency. QTouch library provides an option to perform touch acquisition on three different frequencies. The DEF\_XXXXCAP\_FREQ\_MODE parameter defined in touch.h file determines mode used by the QTouch library for touch acquisition. FREQ\_MODE\_HOP mode uses three frequencies to perform touch acquisition and applies median filter on the resulting three signal values. The user can configure the frequencies on which the QTouch library performs touch acquisition. The user can select three frequencies from a list of sixteen frequencies using DEF\_MUTLCAP\_HOP\_FREQS parameter in touch.h file.

More information on these parameters can be found in [Atmel QTouch Library Peripheral Touch Controller User Guide](#).

Combination of three different frequencies and median filter helps in eliminating the noise level in most of the cases. To identify the correct frequencies for a given system, the user needs to iterate with different combination of frequencies and select the best possible combination that show less noise levels. Better noise rejection can be achieved by using combination of spread-apart frequencies. For example, a combination of frequencies 2, 4, and 8 will reject noise better than compared to a combination of frequencies 4, 5, and 6.

Following figure shows the effect of noise on delta value between FREQ\_MODE\_NONE and FREQ\_MODE\_HOP.

**Figure 2-15. Signal Value Noise vs. Frequency Mode**



**Table 2-5. Frequency Mode Related Parameters**

Parameter	File
DEF_XXXXCAP_FREQ_MODE	Touch.h
DEF_XXXXCAP_HOP_FREQS	Touch.h

#### 2.2.5.7. Frequency Auto Tune

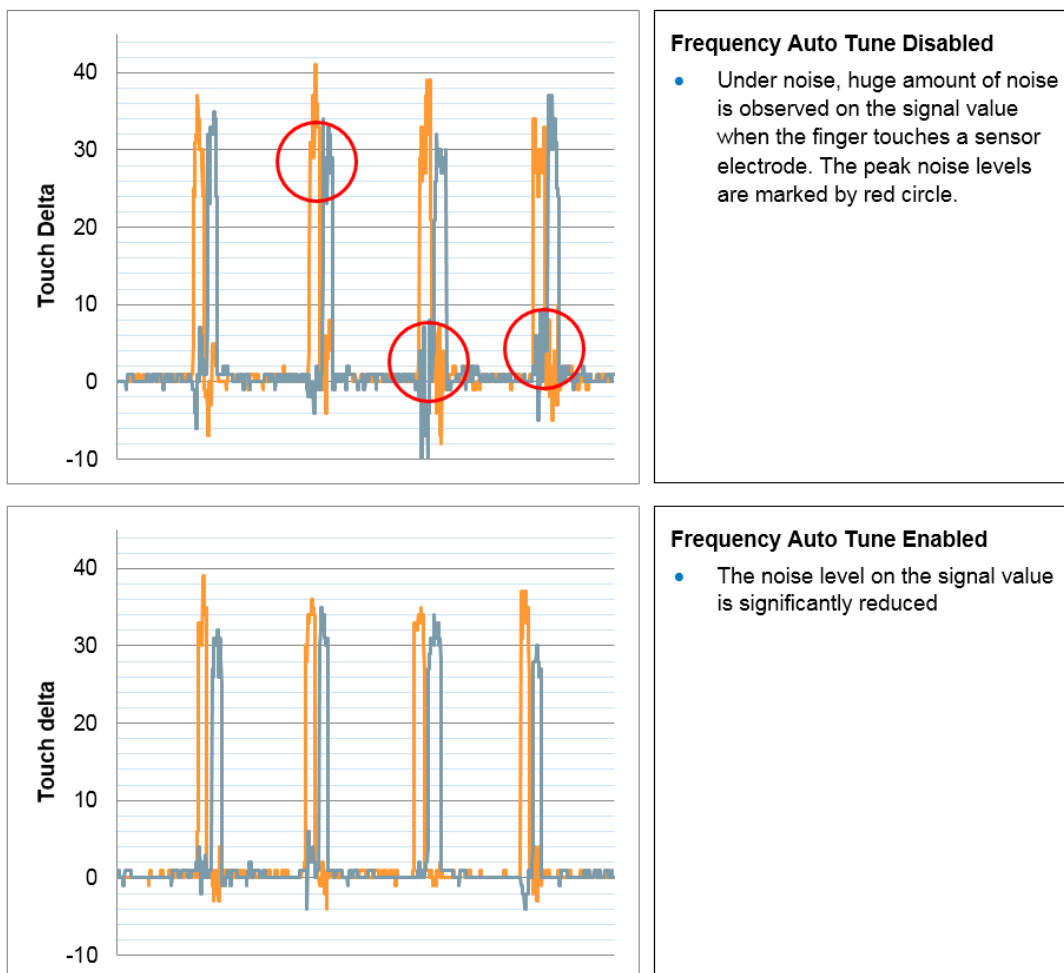
Frequency Auto Tune is an advanced feature provided by QTouch Library. This feature measures the noise on the frequencies used in `FREQ_MODE_HOP` and dynamically replaces the noisiest frequency. This feature is helpful if the system is subjected to different noises of varying frequencies. The frequency switch happens if the signal value is unstable and exceeds the stability limit consistently for auto tune count times. `DEF_XXXXCAP_FREQ_AUTO_TUNE_SIGNAL_STABILITY_LIMIT` sets the stability limit and `DEF_XXXXCAP_FREQ_AUTO_TUNE_IN_CNT` sets the auto tune count.

More information on these parameters are available in [Atmel QTouch Library Peripheral Touch Controller User Guide](#).

**Note:** The frequency auto tune feature is applicable only if `FREQ_MODE_HOP` frequency mode is used.

Following figure shows the difference in noise level observed on touch delta values with and without the Frequency Auto Tune feature.

**Figure 2-16. Delta Value Noise vs. Frequency Auto Tune**



**Table 2-6. Frequency Auto Tune Related Parameters**

Parameter	File
DEF_XXXXCAP_FREQ_AUTO_TUNE_ENABLE	Touch.h
DEF_XXXXCAP_FREQ_AUTO_TUNE_SIGNAL_STABILITY_LIMIT	Touch.h
DEF_XXXXCAP_FREQ_AUTO_TUNE_IN_CNT	Touch.h

The SAM D20 QTouch Robustness demo uses Frequency Auto Tune feature with the following configuration.

#### Mutual Capacitance Sensors

- DEF\_MUTLCAP\_FREQ\_AUTO\_TUNE\_SIGNAL\_STABILITY\_LIMIT = 10
- DEF\_MUTLCAP\_FREQ\_AUTO\_TUNE\_IN\_CNT = 12

#### Self-Capacitance Sensors

- DEF\_SELFCAP\_FREQ\_AUTO\_TUNE\_SIGNAL\_STABILITY\_LIMIT = 20
- DEF\_SELFCAP\_FREQ\_AUTO\_TUNE\_IN\_CNT = 12

### 3. Moisture Tolerance

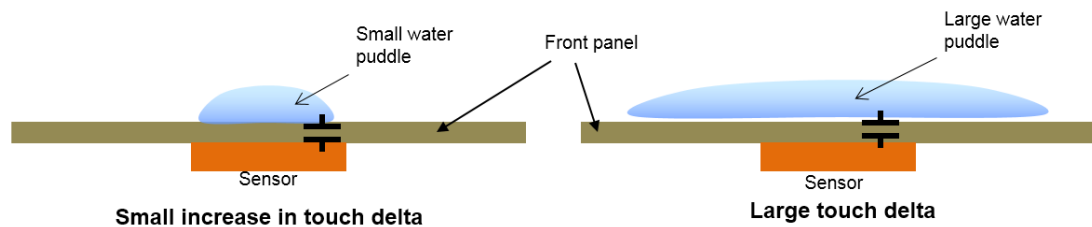
The PTC measures the change in capacitance over a sensor electrode to determine touch. Water is conductive in nature. Interaction of moisture with sensor or the sense lines causes a change in capacitance. This causes disturbances in the capacitive measurements and might lead to erratic behavior of the touch panel. A conductive film of water acts very much like a human finger and can cause false touch detection.

#### 3.1. Impact of Moisture on Capacitive Touch Sensors

It is generally observed that exposing touch sensor to moisture increases the delta values, which eventually lead to false detects. This is true for both self-capacitance and mutual capacitance sensors, but there is slight difference in the way the moisture affects both of these methods.

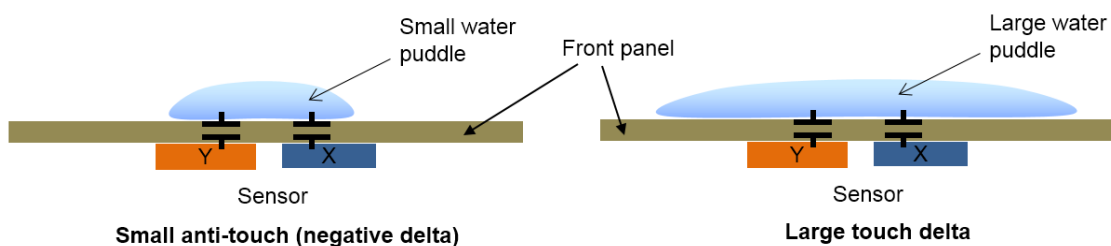
In self-capacitance, water accumulation over sensor increases the effective area of the sensor electrode. This increases the overall capacitance of the sensor as well as increases the coupling with ground. With increase in quantity of water the change in signal values exceeds the detect threshold and lead to false detect.

**Figure 3-1. Effect of Moisture on Self-Capacitance Sensor**



In mutual capacitance, water accumulation over the sensor increases the coupling between the X and Y electrodes. This increased coupling leads to a reduction in the electrode capacitance. Thus an isolated puddle of water over the mutual capacitance sensor shifts the signal to produce negative delta values. This is called as anti-touch effect. However as the water puddle increases in size, it couples with ground and leads to increase in delta values, thereby leading to a false detect.

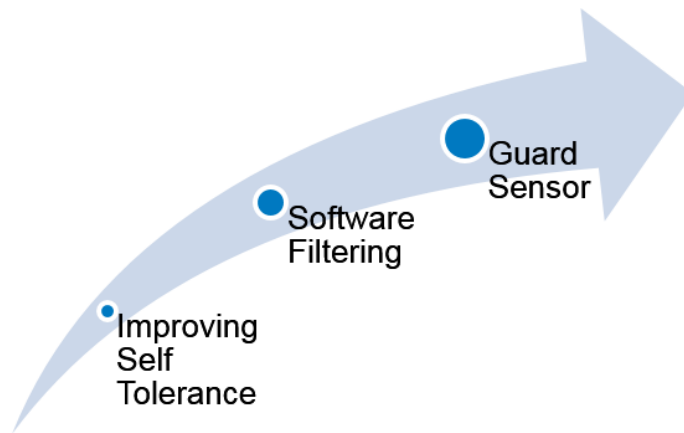
**Figure 3-2. Effect of Moisture on Mutual Capacitance Sensor**



#### 3.2. Improving Moisture Tolerance

Following aspects can be looked into for improve moisture tolerance.

- Hardware Design for improved self-tolerance to moisture
- Software Filtering
- Guard Sensor



### 3.2.1. Improving Self Tolerance

To improve the moisture tolerance of a touch panel, it is necessary to improve its self-tolerance so that it can reject a certain amount of moisture without any special implementations.

- **Selecting Touch Acquisition Method**
  - The mutual capacitance sensors have a natural ability to reject moisture due to the anti-touch effect. The use of mutual capacitance sensors should be preferred in designs where moisture tolerance is a concern. Water drops or small puddles can be handled by mutual capacitance sensor design itself.
  - Ground fill or other lines should be avoided near the sensor area. The ground plane offers very easy path for water droplets to bridge and cause capacitive changes that appear similar to a normal touch.
  - Reducing ground in the PCB decreases the overall noise immunity of the design. A careful compromise has to be done.
- **Tune Detect Threshold**
  - The Detect Threshold should be kept as high as possible
- **Mechanical Design**

When a touch panel is intended to work in conditions like water spill or rain, the below mechanical recommendations should be considered.

- Mounting the touch panel at an inclined angle or vertically mounted is highly beneficial since it allows the water to flow down and thereby prevents water accumulation on panel surface
- Size of sensor electrodes should not be made very large compared to fingers. A large sensor allows more area for the moisture to interact and reduce the finger sensitivity.
- Use of concave touch panel surface above the sensor electrode instead of flat surfaces to avoid water to stay on the button surface.

### 3.2.2. Software Filtering

Avoiding false detects due to large water presence (or water flow) is very challenging. A software filter can be implemented to monitor delta on all sensors and take appropriate measures based on the delta changes.

The idea is to ignore all touches in case there are ambiguous deltas on surrounding sensors and locks the panel if surrounding sensors delta changes is high. This is helpful in case of sudden water spill on the touch panel as it causes increase in delta on many sensors.



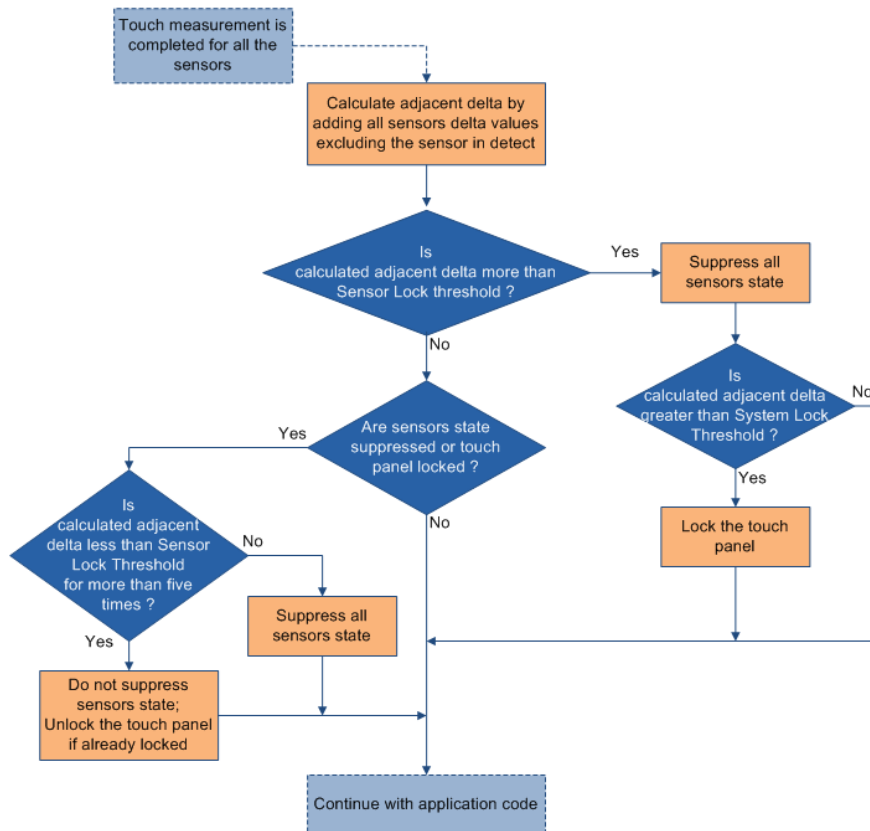
The use case for this filter depends on the nature of the application and the physical configuration of the sensors on the touch panel. This is an effective solution for designs which have sensors spaced closer to each other and requires minimal or no simultaneous touch detection on multiple sensors.

### Adjacent Delta Monitoring

The following flow chart illustrates how the adjacent delta monitoring can be used to prevent false detects due to water presence.

**Note:** The **Adjacent Delta Monitoring** explained in this flow chart is implemented in QTouch library. Refer to [Atmel QTouch Library Peripheral Touch Controller User Guide](#) for information on how to configure and use this feature.

**Figure 3-3. Flow Chart – Adjacent Delta Monitoring**



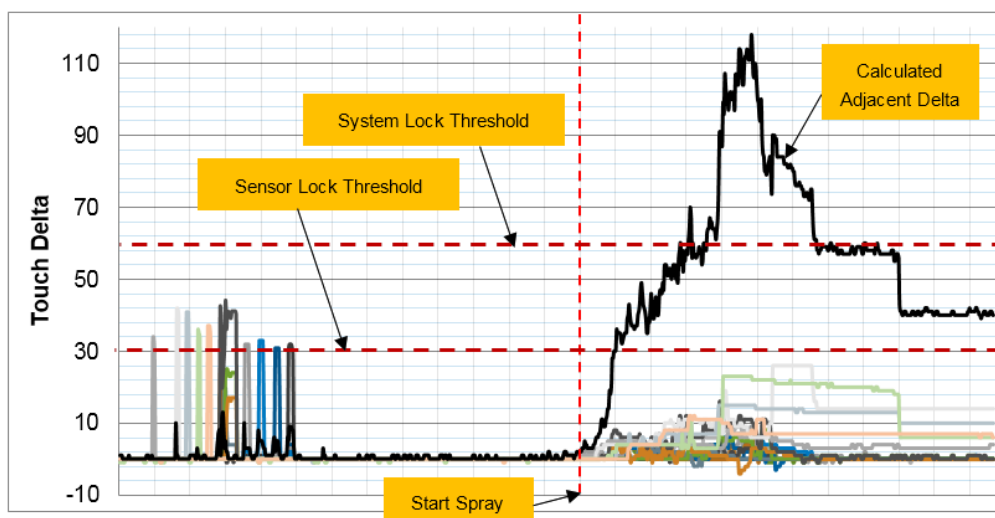
The adjacent delta monitoring can be performed on a group of sensors that are physically closer to each other. The above flow chart shows adjacent delta monitoring for one group. The same can be repeated for multiple groups of sensors.

There may be condensation on PCB due to fast changes in the ambient temperature. This may cause larger changes in capacitance on the sensor traces and causing false detects. Adjacent delta monitoring is an effective solution to prevent false detects due to PCB condensation.

The SAM D20 QTouch Robustness demo uses adjacent delta monitoring technique to improve moisture tolerance.

Following figure shows how the adjacent delta monitoring suppresses false detects during water spray over the touch panel.

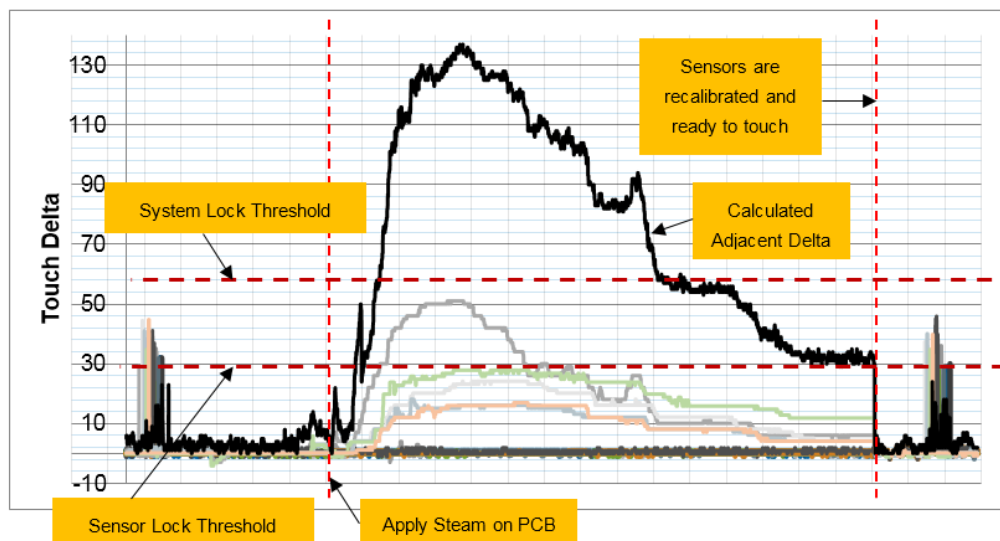
**Figure 3-4. Effect of Water Spray Over the Touch Panel**



The adjacent delta value increases as soon as the water is sprayed over the touch panel. The sensor states are suppressed when adjacent delta value reaches the threshold (say 30 counts). With additional spraying, there is further increase in adjacent delta. The panel is locked when adjacent delta reaches a value twice the threshold (say 60 counts).

Following figure shows how the adjacent delta monitoring suppresses false detects when steam is applied over the PCB.

**Figure 3-5. Effect of Steam Over PCB**

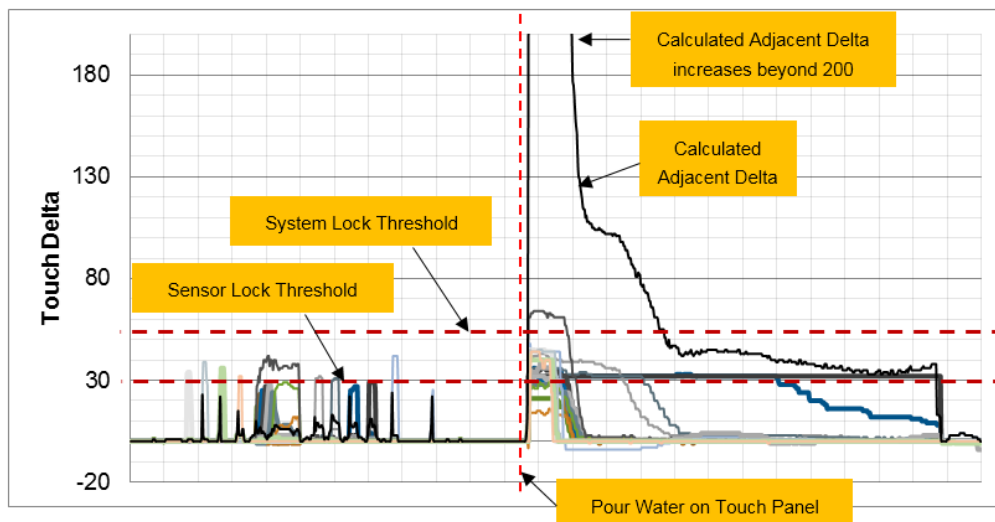


The adjacent delta value increases as soon as the steam is applied on the PCB. The sensor states are suppressed when adjacent delta value reaches threshold (say 30 counts). With more steam, there is further increase in adjacent delta. The panel is locked when adjacent delta value reaches a value twice the threshold (say 60 counts).

When steam is removed, eventually water droplets from the PCB will evaporate and the delta reduces. When the adjacent delta goes below the threshold (say 30 counts) the sensors are re-calibrated and the touch panel is ready for normal operation.

Following figure shows how the adjacent delta monitoring suppresses false detects when huge amount of water is poured over the touch panel.

**Figure 3-6. Water Pour Over Touch Panel**



The adjacent delta value increases rapidly when huge amount of water is poured over the touch panel. The panel is locked when adjacent delta value reaches two times the threshold (say 60 counts). When water is wiped, the adjacent delta reduces and goes below the threshold (say 30 counts). The sensors are re-calibrated and the touch panel is ready for normal operation.

### 3.2.3. Guard Sensor

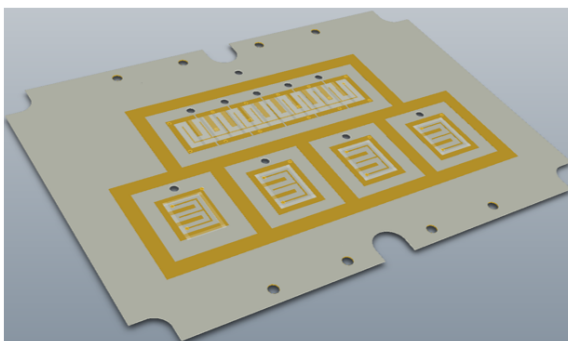
The use of a guard sensor is an effective solution for detecting different forms of moisture formations on the touch panel. The guard sensor is a special sensor that is strategically located and has slightly higher sensitivity. If any unintentional touch occurs, the guard sensor is activated. This indicates that a false detect has occurred. Usually, the electrode for the guard sensor is a conductive fill that surrounds the electrodes for other sensors.

In a moisture tolerant touch design, the guard sensor plays an important role in detecting the presence of water and helps to suppress the false detects. An oversensitive guard sensor will have proximity effects and an under-sensitive guard sensor will not be able to detect the presence of moisture accurately. This increases the complexity of designing the guard sensor to get the right balance.

Since guard sensor is a large electrode and has higher sensitivity, it will be more sensitive to noise. In such cases it must be carefully designed to avoid larger guard sensor electrodes.

Following figure shows an example of how a guard sensor can be realized.

**Figure 3-7. Example Guard Sensor**

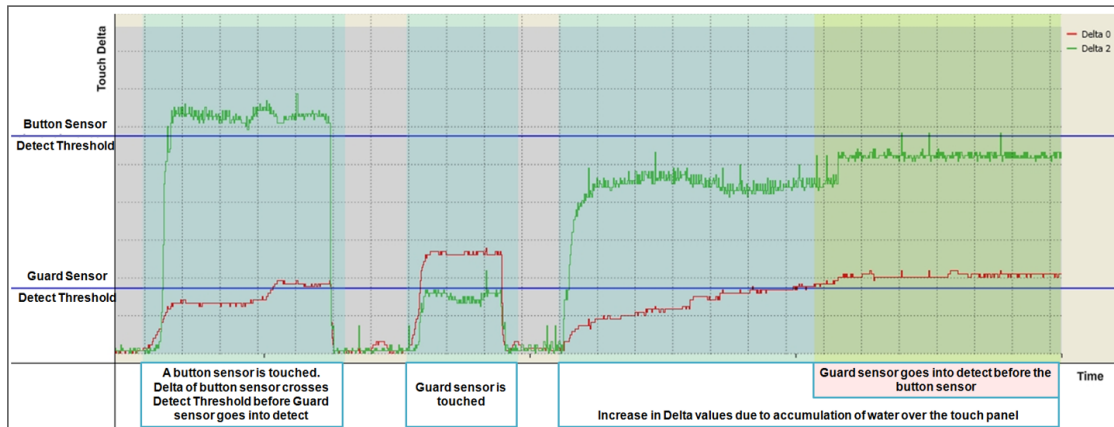


### 3.2.3.1. Tuning Guard Sensor for Moisture Tolerance

Performance of any capacitive touch interface depends upon how well it has been tuned. For moisture tolerant touch designs there are some special considerations.

Following figure is a graph recorded from a well-tuned system for a button and guard sensors. The green plot represents the delta values of a button and the red plot represents the delta values of the guard sensor. The detect threshold for the button as well as the guard sensor has been represented by the blue lines. The three events in the graph are highlighted in light blue and are explained below.

**Figure 3-8. Touch Delta Variation in Guard and Button**



In the first event, the button is touched. It is observed that the delta value for that button quickly crosses its detecting threshold and reports touch detection. Due to proximity effect there is a rise in the delta value of the guard as well, but the delta value is not high enough to cross the detect threshold for the guard.

In the next case the guard sensor is touched. The guard sensor immediately crosses its detect threshold. Some rise in delta value is observed in the button due to proximity effect.

In the last event, water is poured over the button and forms a water puddle spreading across the touch panel. The delta values start to steadily increase in both the guard sensor as well as the button. The delta value is proportional to the amount of water accumulated over the button. After a certain amount of water accumulation, the delta value of the guard sensor reaches its detect threshold.

More information about Guard Sensor implementation can be found in [AVR3002: Moisture Tolerant QTouch Design](#) application note.

## 4. Touch Response Time

The time taken by the application to react to a finger touch is the response time. This time includes the time taken by the microcontroller to perform the touch acquisition and post-processing to report a valid touch. Better user experience can be achieved with lower response time. With lower response time, the number of touch detections increases per second.

### 4.1. Ideal Response Time

The required response time depends on the application. For applications such as computer keyboard or a game console extremely fast response time is expected. Whereas, a washing machine can provide adequate user experience with much longer response times.

It is highly impossible for a human to touch a button more than eight times per second. Detecting one touch per ~125msec is good enough for user interface applications.

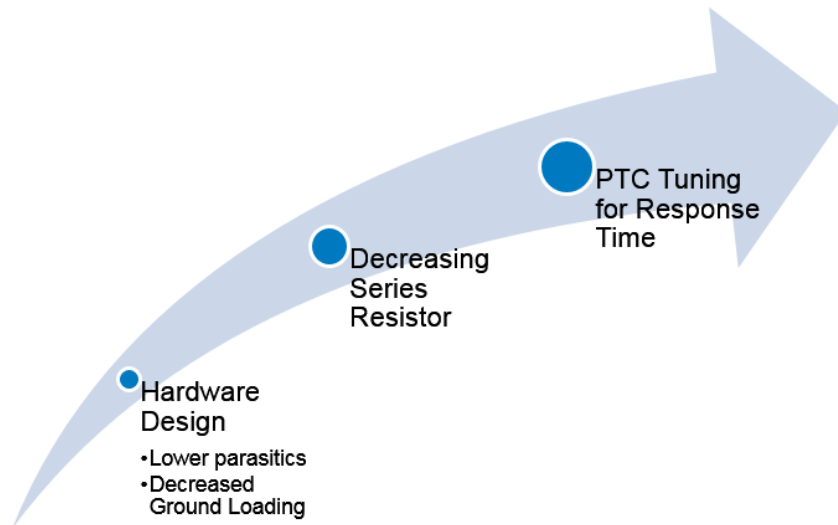
It is possible for a human finger to move over the slider/wheel much faster compared to touching a button. If application has sliders/wheels, then lesser response time provides better user experience.

If the delay between a finger touch and any visual indication (say LED illumination) is more than ~70msec (16 frames per seconds), then this may be perceived as a delay. Applications which provide visual indications require response time less than 70msec.

### 4.2. Improving Response Time

The following factors affects touch response time:

- Hardware Design
- Optimizing Series Resistor
- PTC Tuning



#### 4.2.1. Hardware Design

Increasing parasitic capacitance ( $C_p$ ) increases touch acquisition time and vice-versa.

To reduce the Parasitic capacitance,

- Reduce the trace length

- Longer sense traces running from the MCU to the sensor electrode increases the parasitic capacitance.
  - In mutual capacitance, avoid routing X- and Y-lines close to each other.
- Have a ground plane beneath the sensor electrode only, if it is required
  - Hatched ground flood can be used in low-noise systems.
- The size of the sensor electrode should be optimum
  - Bigger sensors have higher parasitic capacitance. So, adequate sensor size should be used.
  - For spring based designs, it is recommended to use thinner wire gauge for springs. The pad size for soldering the spring should be optimum to minimize the amount of solder.

#### 4.2.2. Optimizing Series Resistor

- Increasing  $R_s$  increases touch acquisition time and vice-versa. Select optimum  $R_s$  value for which system performance is acceptable.
- Avoid through-hole resistor as it increases parasitic capacitance.

#### 4.2.3. Increasing CPU Clock

- Increasing CPU clock frequency reduces post-processing time. The CPU should be clocked at the maximum clock frequency to get reduced response time and better user experience.

#### 4.2.4. PTC Tuning for Response Time

The QTouch Library provides various parameters for optimized touch performance. By properly tuning these parameters, the best possible response time can be achieved.

**Table 4-1. PTC Tuning for Response Time**

Feature	QTouch Library Parameter	Recommendation
Filter Level	DEF_XXXXCAP_FILTER_LEVEL	<ul style="list-style-type: none"> <li>• All applications do not require filter level 64</li> <li>• Reducing the filter level from 64 to 32 would reduce the touch acquisition time by almost half</li> <li>• Reducing the Filter Level reduces touch response time</li> </ul>
Auto Over Sample	DEF_XXXXCAP_AUTO_OS	<ul style="list-style-type: none"> <li>• Auto Over Sample does additional touch acquisition while touching and removing finger. This increases touch response time.</li> <li>• Enable the Auto OS feature only if it is “really” required. If the Auto OS feature is used, then use an appropriate Auto OS level.</li> <li>• Reducing the Auto OS level reduces touch response time.</li> </ul>

Feature	QTouch Library Parameter	Recommendation
Frequency Mode	DEF_XXXXCAP_FREQ_MODE	<ul style="list-style-type: none"> <li>FREQ_MODE_HOP applies median filter on the signal value. When the sensor is touched, due to median filter, it needs additional one touch acquisition cycle to start the DI process. This increases the response time by one cycle time.</li> <li>Some level of noise can be tackled by adjusting the filter level and Auto OS.</li> <li>Applications which require extreme noise mitigation only should use FREQ_MODE_HOP.</li> </ul>
Clock Prescaler	DEF_XXXXCAP_CLK_PRESCALE	<ul style="list-style-type: none"> <li>Increasing the pre-scale value increases the touch acquisition time and vice-versa. Higher electrode capacitance (Cx) or higher series resistor (Rs) requires slower clock (higher pre-scale value) for complete charging.</li> <li>Increasing the pre-scale level from one to two increases the touch acquisition time.</li> </ul>
Charge Share Delay (available only for SAM C2x, SAM L22 and ATmega328PB devices)	DEF_XXXX_CAP_CSD_VALUE	<ul style="list-style-type: none"> <li>Increasing the CSD parameter increases the touch acquisition time and vice-versa. Higher electrode capacitance (Cx) or higher series resistor (Rs) requires slower clock (higher CSD) for complete charging.</li> <li>To reduce the touch measurement time, tune the CSD parameter with DEF_XXXXCAP_CLK_PRESCALE configured to one.</li> </ul>
Internal Series Resistor	DEF_XXXXCAP_SENSE_RESISTOR	<ul style="list-style-type: none"> <li>Increasing series resistor value may require increase in prescaler value.</li> <li>Set internal resistor value to zero if only external resistor is used.</li> </ul>

Feature	QTouch Library Parameter	Recommendation
Detect Integration	DEF_XXXXCAP_DI	<ul style="list-style-type: none"> <li>Increasing DI increases repeated acquisition during touch and removing finger. This increases touch response time.</li> <li>DI can be reduced to two in low-noise systems.</li> </ul>
Frequency Auto Tune	DEF_XXXXCAP_FREQ_AUTO_TUNE_SIGNAL_STABILITY_LIMIT	<ul style="list-style-type: none"> <li>Setting “lower” value for these parameters would trigger unwanted auto frequency tuning and increase the touch response time.</li> <li>Set proper value such that auto frequency tuning is triggered only for appropriate noise level.</li> </ul>
	DEF_XXXXCAP_FREQ_AUTO_TUNE_IN_CNT	
Quick Re-burst	DEF_XXXXCAP_QUICK_REBURST_ENABLE	<ul style="list-style-type: none"> <li>Resolving calibration or re-calibration or DI requires multiple touch acquisitions.</li> <li>Enabling quick re-burst feature does repeated touch acquisitions only on sensors which requires multiple touch acquisitions.</li> <li>Enabling quick re-burst feature reduces touch response time.</li> </ul>
PTC Interrupt Priority	DEF_TOUCH_PTC_ISR_LVL	<ul style="list-style-type: none"> <li>The PTC interrupt priority level decides how fast the QTouch library services PTC’s End-of-Conversion interrupt.</li> <li>Lower priorities would increase the touch response time.</li> </ul>
Scan Rate	DEF_TOUCH_MEASUREMENT_PERIOD_MS	<ul style="list-style-type: none"> <li>Increasing sampling interval reduces the user experience.</li> <li>Optimum value depends on the application and the time taken to perform touch acquisition on all configured sensors.</li> <li>Minimum value is the time taken to perform touch acquisition on all configured sensors.</li> </ul>



## 5. References

[1] AVR3000: QTouch Conducted Immunity

<http://www.atmel.com/Images/doc8425.pdf>

[2] QTAN0079 Buttons, Sliders and Wheels - Sensor Design Guide

<http://www.atmel.com/Images/doc10752.pdf>

[3] Peripheral Touch Controller - User Guide

[http://www.atmel.com/images/atmel-42195-qtouch-library-peripheral-touch-controller\\_user-guide.pdf](http://www.atmel.com/images/atmel-42195-qtouch-library-peripheral-touch-controller_user-guide.pdf)

[4] AVR3002: Moisture Tolerant QTouch Design

<http://www.atmel.com/images/doc42017.pdf>

[5] IEC Standards – 61000-4 Series

<http://www.iec.ch>

[6] AVR040: EMC Design Considerations

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## 6. Revision History

Doc Rev.	Date	Comments
42360C	05/2016	Added information about Charge Share Delay (CSD) parameter in sections <a href="#">PTC Tuning</a> and <a href="#">PTC Tuning for Response Time</a> .
42360B	04/2015	<ul style="list-style-type: none"><li>• Y0 and Y2 related note is updated.</li><li>• Guidelines relate do routing and GND shieling on edges of PCB are updated.</li><li>• Moisture flow chart is updated.</li></ul>
42360A	11/2014	Initial document release.

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