Managing dynamic input power with the STUSB4500 and the STM32F072RB

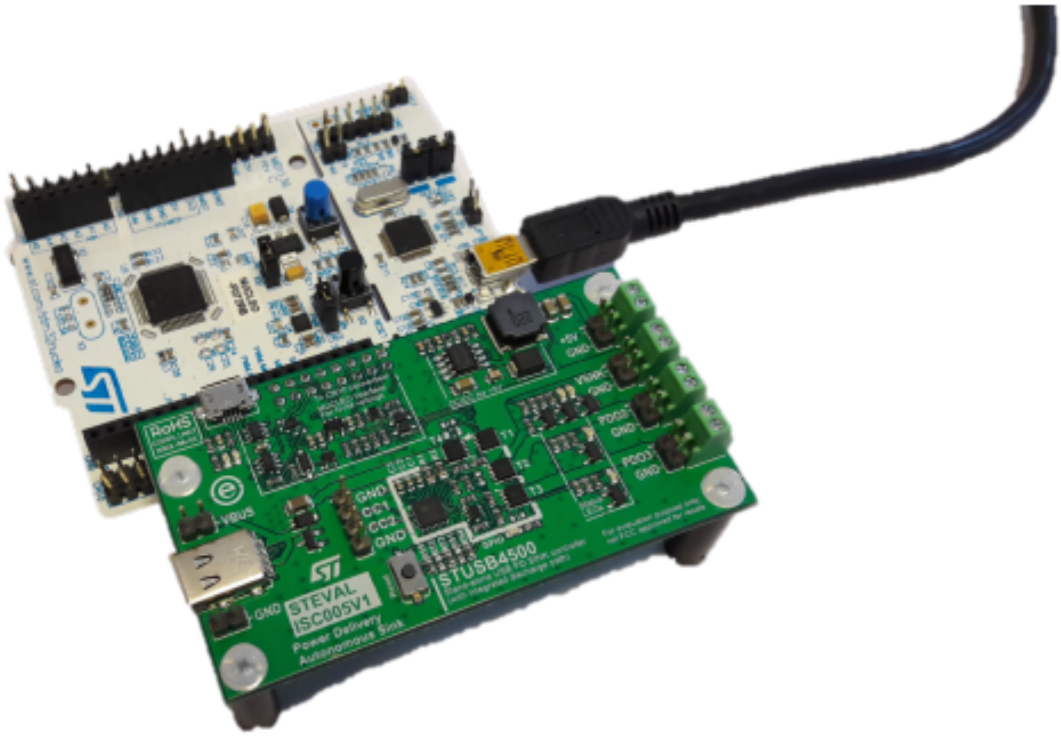
Introduction

This document describes how to manage the dynamic input power by applying software on top of the STUSB4500 from the application processor (in this document, an STM32F072RB). Open source software is available to speed-up end-application SW developments. The code is available as an example only.

Table 1. Minimal configuration

NUCLEO-F072RB	STM32 Nucleo-64 development board with AMR Cortex M0
STEVAL_ISC005V1	STUSB4500 evaluation board
STSW-STUSB003	Software library including the STUSB4500 hardware abstraction layers, drivers and Code example
IAR 8.x	C code compiler

Figure 1. NUCLEO-F072RB+STEVAL_ISC005V1



1 Hardware configuration

The STEVAL_ISC005V1 must be plugged to the NUCLEO-F072RB board according to the schematic below:

Figure 2. NUCLEO-F072RB connections

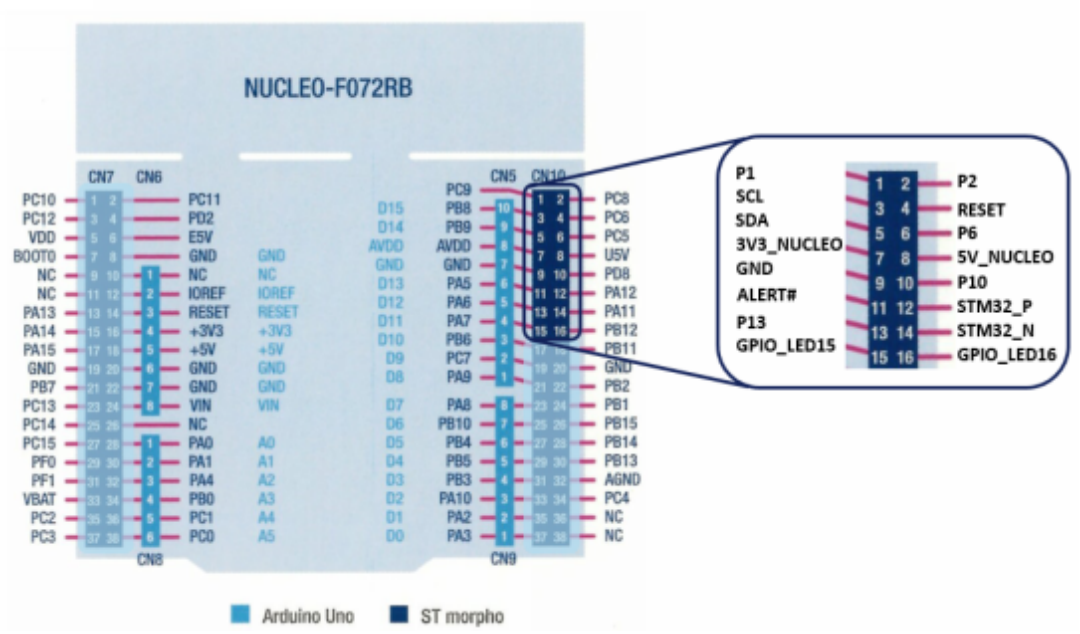
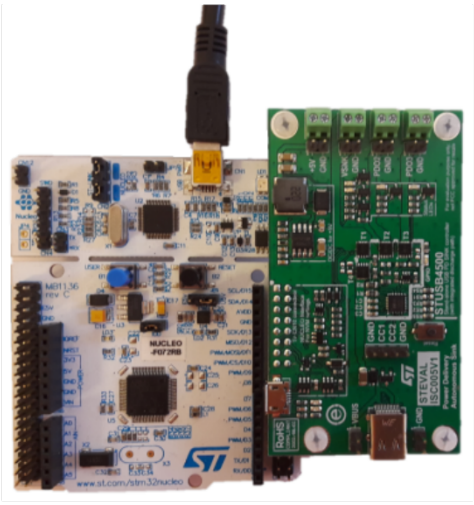


Figure 3. STEVAL_ISC005V1 plugged to the NUCLEO-F072RB



2 Software configuration

2.1 Main files

The library is composed of the following files:

- USB_PD_defines_STUSB-GEN1S.h
- USB_PD_core.h
- USB_PD_core.c
- Main.c

USB_PD_defines_STUSB-GEN1S.h: contains the register definition from the STUSB4500.
USB_PD_core.h: contains main structures to handle the STUSB4500 configuration.
USB_PD_core.c: contains generic functions to build applications.
Main.c: illustrates how to use the functions and build applications.

3 Low level drivers, hardware abstraction functions

Here is the list of available functions developed to show the STUSB4500 software capabilities:

```
void usb_pd_init(uint8_t Port);
void ALARM_MANAGEMENT(uint8_t Port);


void HW_Reset_state(uint8_t Port);
void SW_reset_by_Reg(uint8_t Port);
void Read_SNK_PDO(uint8_t Port);
void Print_SNK_PDO(uint8_t Port);
void Print_PDO_FROM_SRC(uint8_t Port);
void Read_RDO(uint8_t Port);
void Print_RDO(uint8_t Port);
void Update_PDO(uint8_t Port, uint8_t PDO_Number, int Voltage, int Current) ;
void Update_Valid_PDO_Number(uint8_t Port, uint8_t Number_PDO);
```

3.1 usb_pd_init(Port)

This function clears all interrupts and unmasks the useful interrupts.

3.2 ALARM_MANAGEMENT(Port)

This is the device interrupt handler.

3.3 HW_Reset_state(Port)

This function asserts and de-asserts the STUSB4500 hardware reset pin. After the reset, the STUSB4500 behaves according to non-volatile-memory default settings.

3.4 SW_Reset_by_Reg(Port)

This function resets the STUSB4500 Type-C and USB PD state machines. It also clears any ALERT. By initializing Type-C pull-down termination, it forces the electrical USB type-C disconnection (both on source and sink sides).

3.5 Read_SNK_PDO(Port);

This function reads the SINK_PDO registers from the STUSB4500 and loads it in a dedicated structure.

3.6 Print_SNK_PDO(Port);

This function calls the Read_SNK_PDO function and prints the PDO values to the serial interface.

3.7 Print_PDO_FROM_SRC (Port);

This function prints the source capabilities received by the STUSB4500. Source capabilities are automatically stored on the device connection in a dedicated structure.

3.8 Read_RDO (Port);

This function reads the requested data object (RDO) register, in order to access contract PDO number.

3.9 Print_RDO (Port);

This function reads the requested data object (RDO) register, and prints the current contract to the serial interface in case of capability match between the STUSB4500 and the source.

3.10 Update_PDO(Port, PDO_number, voltage, current)

This function can be used to overwrite PDO2 or PDO3 content in RAM.

Arguments are:

- I²C port number
- PDO index to be updated: 2 (for PDO2) or 3 (for PDO3)
- Voltage (in mV) truncated by 50 mV
- Current (in mA) truncated by 10 mA

3.11 UPDATE_Valid_PDO_Number(Port, Number_PDO)

This function is used to overwrite the number of valid PDO.

Arguments are:

- I²C port number
- active PDO number: from 1 to 3

4 Application example

4.1 Set_New_PDO_case1(Port);

The sample function sets PDO2 and PDO3 to 15 V/1.5 A and 20 V/1.5 A respectively.

4.2 Negotiate_5V(Port);

The sample function reconfigures the PDO number to only one, so by default PDO1. In this manner, the STUSB4500 negotiates 5 V back with the source.

4.3 Find_Matching_SRC_PDO(Port, Min_Power, Min_V, Max_V);

This function can be used to check if one of the source PDO (the sink is connected to) is compatible with the sink application. In such case, a PDO from the sink can be defined dynamically with compatible parameters in order to renegotiate at the compatible voltage node. In practice, the function scans the source PDO (received on connection). If one of the source PDO falls within the range of the function arguments, ie. within a voltage range (min. and max.) and with enough current at the given voltage, then it redefines the SINK_PDO3 with the identified and matching source PDO. This allows the STUSB4500 to best match the source capabilities.

5 Demo

5.1 STUSB4500_PDO_rolling_DEMO.bin

This demo sequentially changes PDO2 and PDO3 to 9 V and 12 V respectively, then 15 V and 20 V respectively. The number of valid PDO is successfully set to 2 and 3. It results a demo in which the STUSB4500 negotiates from 5 V to 9 V then 12 V then 15 V then 20 V, then back to 15 V then 12 V, then 9 V etc.. etc..

This demo has been implemented by using a combination of a timer calling the previously defined functions.

Revision history

Table 2. **Document revision history**

Date	Version	Changes
05-Sep-2018	1	Initial release.

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